**DAILY ASSESSMENT FORMAT**

|  |  |  |  |
| --- | --- | --- | --- |
| **Date:** | **28-05-2020** | **Name:** | **HEMALATHA SANIL** |
| **Course:** | **Logic Design** | **USN:** | **4AL17EC035** |
| **Topic:** | **1.Boolean equations for digital circuits.**  **Combinational circuits: Conversion of MUX and Decoders to logic gates.**  **2.Design of 7 segment decoder with common anode display.** | **Semester & Section:** | **6 SEM & ‘A’ SEC** |
| **Github Repository:** | **Hemalatha-Sanil** |  |  |

|  |
| --- |
| **FORENOON SESSION DETAILS** |
| **Image of session** |
| **Report – Report can be typed or hand written for up to two pages.** |

|  |  |  |  |
| --- | --- | --- | --- |
|  |  |  |  |
|  |  |  |  |

|  |  |  |  |
| --- | --- | --- | --- |
| **Date:** | **28-05-2020** | **Name:** | **HEMALATHA SANIL** |
| **Course:** | **PYTHON** | **USN:** | **4AL17EC035** |
| **Topic:** | **Section 23** | **Semester & Section:** | **6 SEM & ‘A’ SEC** |
| **Github Repository:** | **Hemalatha-Sanil** |  |  |

|  |
| --- |
| **AFTERNOON SESSION DETAILS** |
| **Image of session** |
| **Report – Report can be typed or hand written for up to two pages.** |

|  |  |  |  |
| --- | --- | --- | --- |
|  |  |  |  |
|  |  |  |  |